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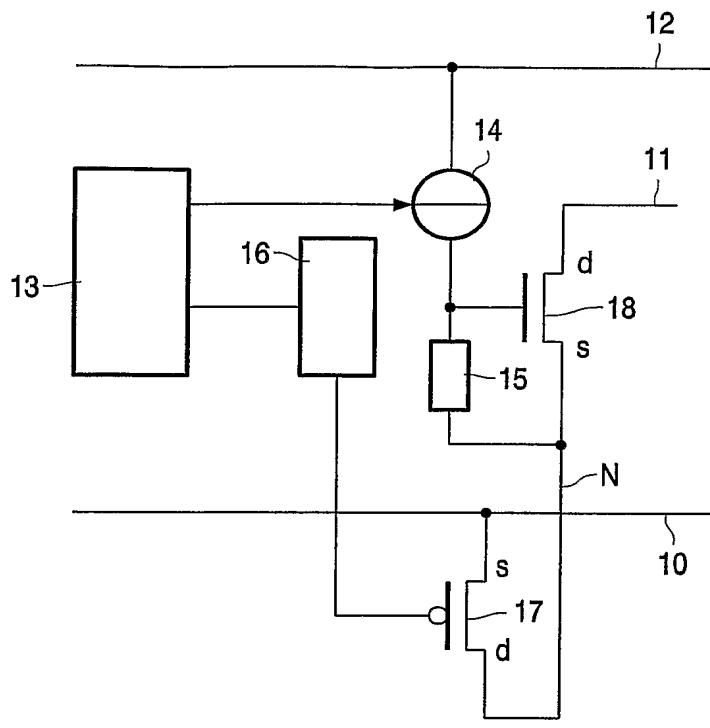
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(54) Title: MOS SWITCHING CIRCUIT



(57) Abstract: An electronic circuit has a signal conductor (11), a power supply reference conductor (10) connected by a switching circuit. The switching circuit contains a PMOS transistor (17) and an NMOS transistor realized on a common substrate (100). The NMOS transistor (17) has a source coupled to the power supply reference conductor (10). The NMOS transistor (18) has a source coupled to the drain of the PMOS transistor (17), and a drain coupled to the signal conductor (11). A control circuit (13, 14, 15, 16) switches between an "on" state and an "off" state, in which the control circuit (13, 14, 15, 16) controls the gate source voltages of the first and second MOS transistor (17, 18) to make channels of these MOS transistors (17, 18) conductive and not to make the channels of these first and second transistors (17, 18) conductive respectively. Preferably a complementary switching circuit is also provided. The complementary switching circuit uses opposite polarity voltage differences, an NMOS transistor (27) coupled to a second power supply and a PMOS transistor (28) coupled to a signal conductor. The on resistances of the switching circuits are matched by matching the NMOS gate-source voltages, as well as the PMOS gate-source voltages.

**Declaration under Rule 4.17:**

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